

Diagram illustrating the 10-bit frame structure for the 100BASE-T4 standard. The frame is divided into three main sections:

- PREAMBLE:** Consists of 5 bits, represented by five 1-bit fields.
- AUX DATA:** Consists of 4 bits, represented by four 1-bit fields.
- SAMPLE DATA:** Consists of 36 bits, represented by a large 36-bit field.

The frame structure is shown as a sequence of fields: PREAMBLE (5 bits), AUX DATA (4 bits), SAMPLE DATA (36 bits), and a final 1-bit field labeled 'V'. The fields are labeled 'V', 'U', 'C', and 'P' at the bottom.

Figure 2 illustrates the data structure of the frame. The frame is divided into two main sections: SUBFRAME 1 and SUBFRAME 2. Each subframe contains a header (HDR) and a data field (DATA). The frame is also divided into two sections, AESA and AESB. The AESA section contains the first subframe, and the AESB section contains the second subframe. The frame is also divided into two sections, SUBFRAME 1 and SUBFRAME 2. The frame is also divided into two sections, SUBFRAME 1 and SUBFRAME 2.

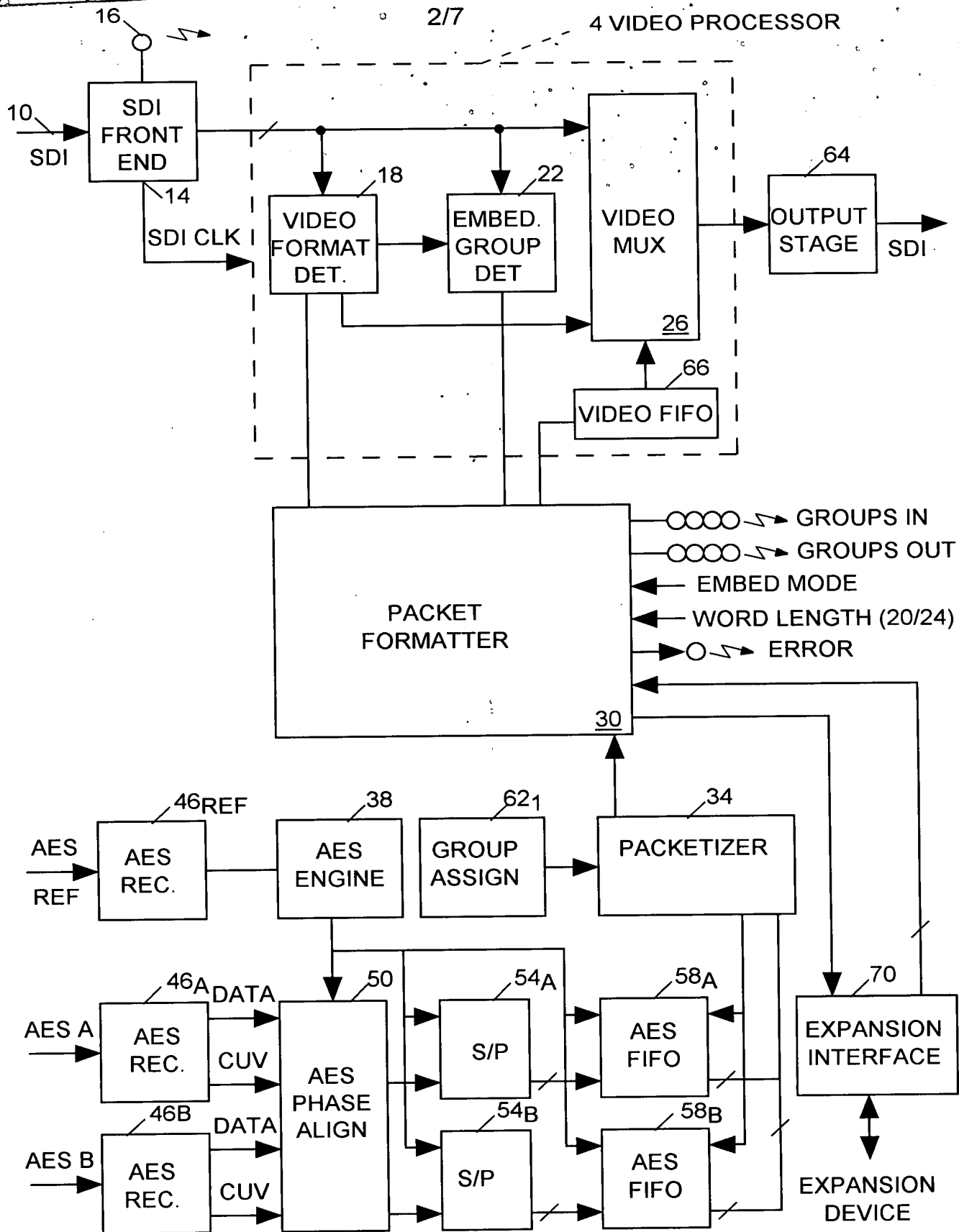


FIG. 3

8 AUDIO PROCESSOR

033474-0330

APPROVED	O.B. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

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66E760" 24246E60

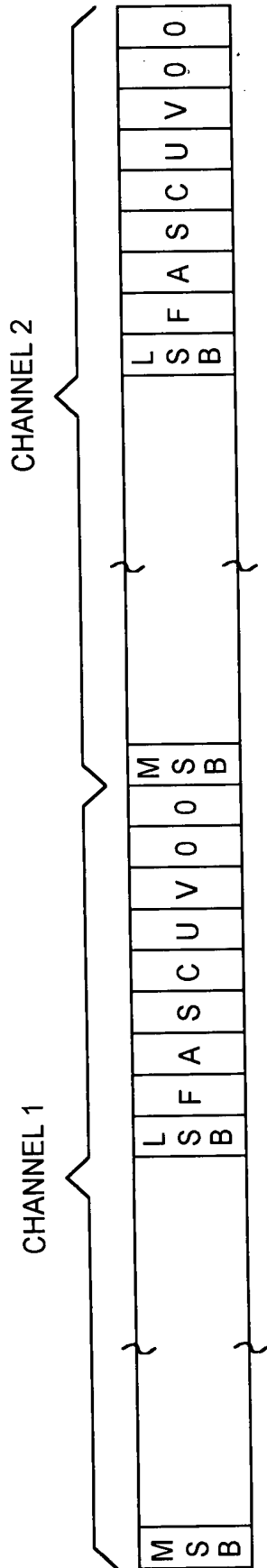


FIG. 4

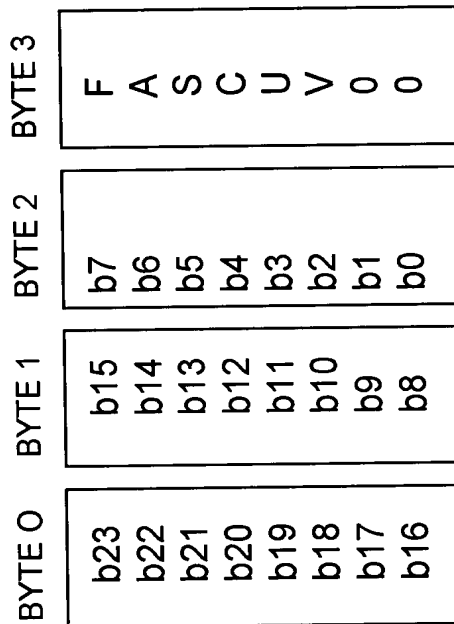


FIG. 5

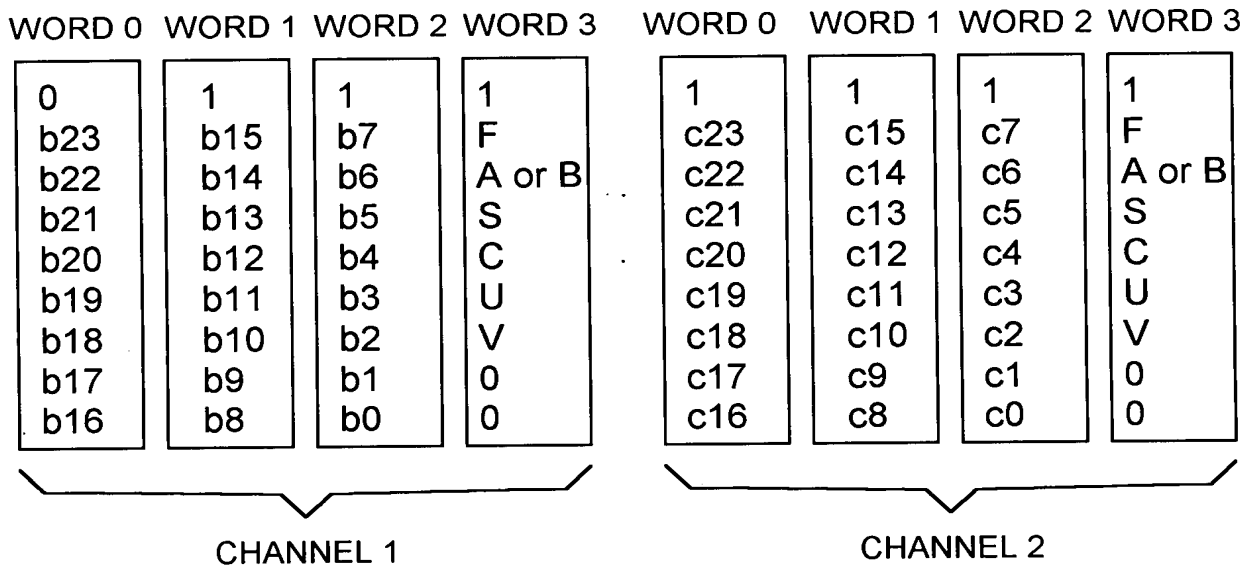


FIG. 6

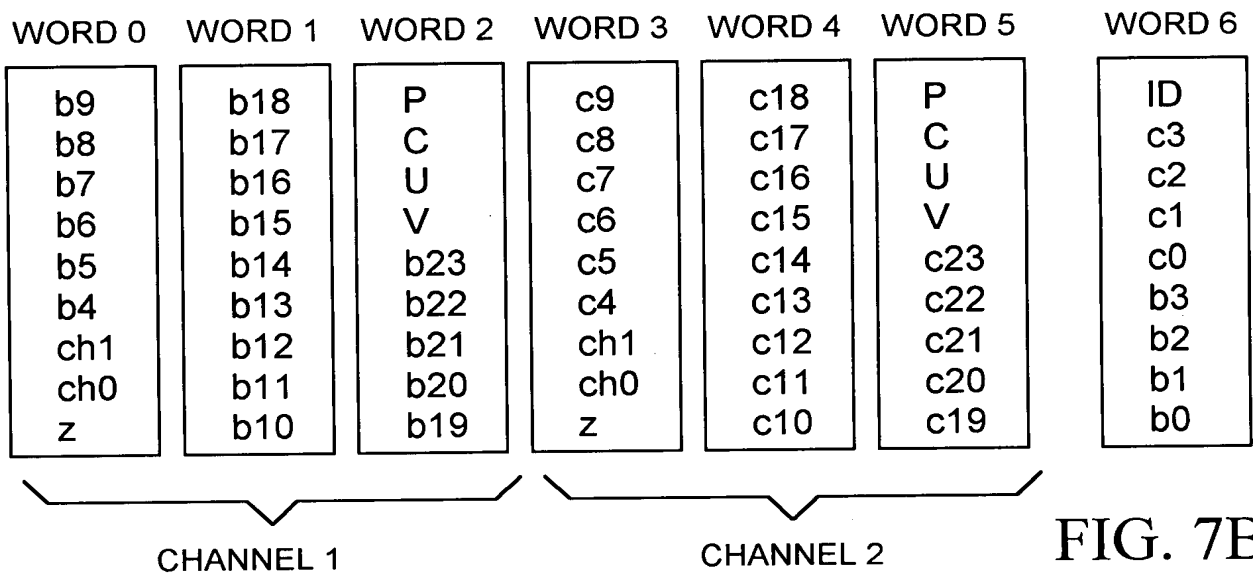


FIG. 7B

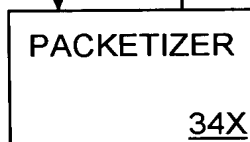
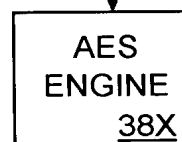
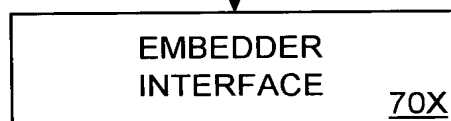
FIG. 7A

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AUDIO
PROCESSOR

8X

EMBEDDER



AES C/D ASSIGN

AES E/F ASSIGN

AES G/H ASSIGN

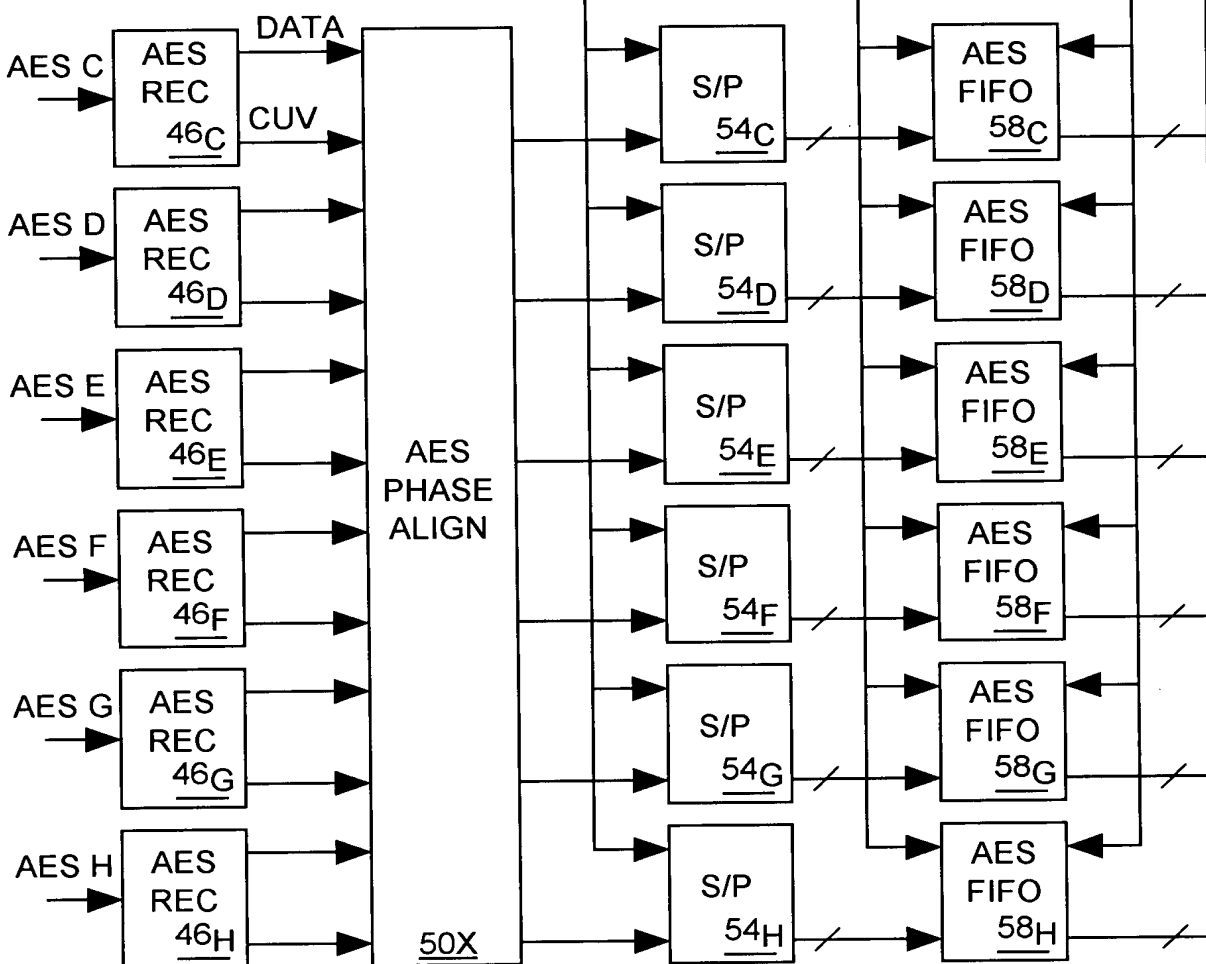


FIG. 8

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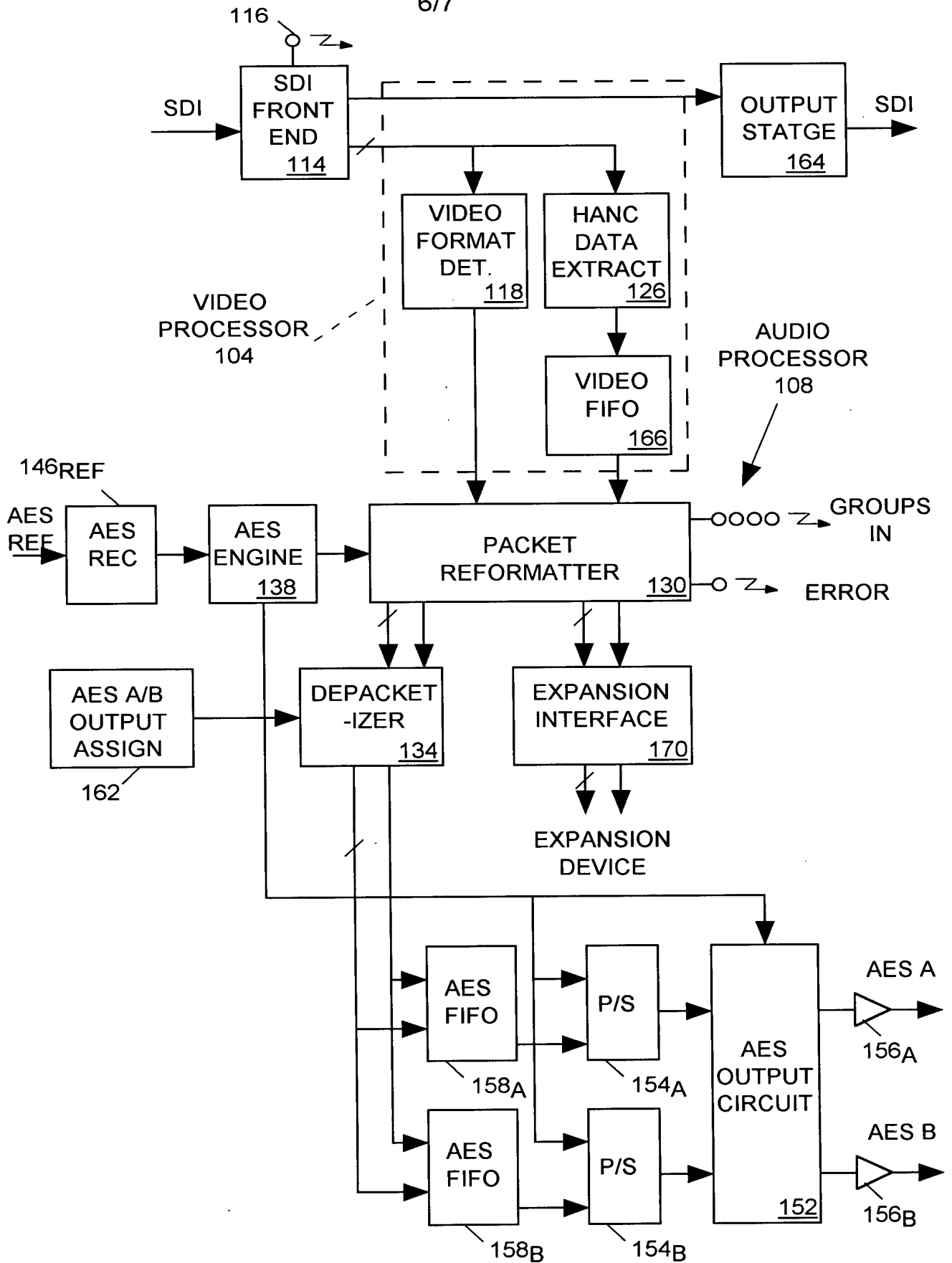


FIG. 9

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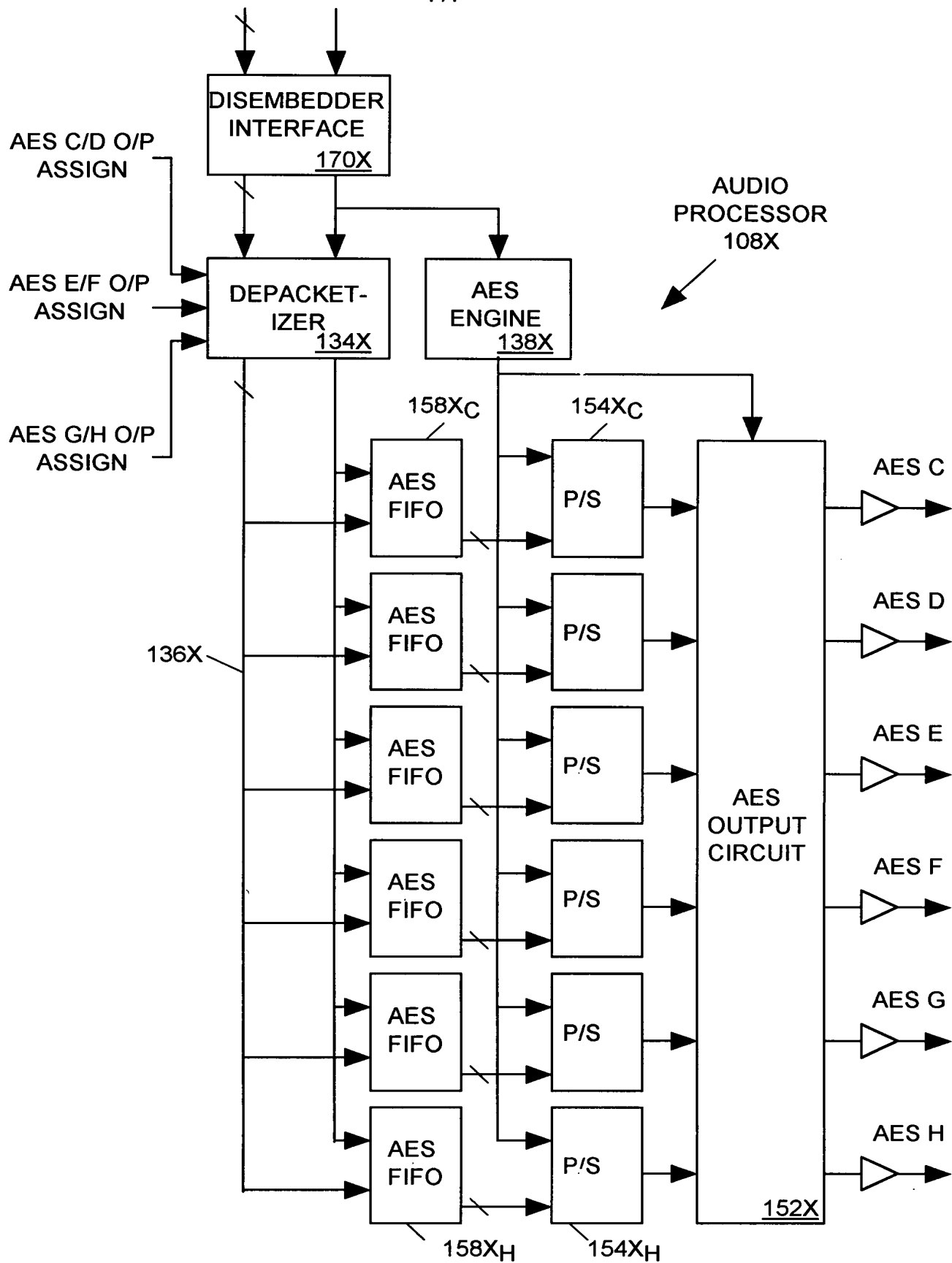


FIG. 10